

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Assignee: Intel Corporation
Inventor: YUNG-HSIANG LEE et al.
Serial No.: TO BE ASSIGNED
Filed: HERewith
Group Art Unit: To Be Assigned (Anticipated 2184)
Examiner: To Be Assigned (Anticipated Dieu Minh T Le)
Title: A TECHNIQUE FOR SYNCHRONIZING FAULTS IN A
PROCESSOR HAVING A REPLAY SYSTEM

BOX PATENT APPLICATION
COMMISSIONER FOR PATENTS
P.O. Box 4250
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

Prior to examination of the above-identified application and calculation of the filing fee, please amend the application as follows.

IN THE SPECIFICATION:

Please insert, on page 1, in the first line of the specification following the title, the following new paragraph.

This application is a continuation of application Serial No. 09/472,839 filed
December 28, 1999.